

1           17. (Unamended) In a computer system having a source computer and a  
2 destination computer having a clock that regulates timing of activities at the destination  
3 computer, a method comprising the steps of:

4           providing a logical structure for encapsulating multiple streams of data, said  
5 streams of data being stored in packets;

6           storing clock licenses that dictate advancement of a clock in multiple ones of the  
7 packets;

8           transmitting the logical structure from the source computer to the destination  
9 computer; and

10          for each packet that holds a clock license, advancing the clock at the destination  
11 computer as dictated by the clock license in response to receiving the packet at the  
12 destination computer.

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14          18. (Unamended) The method of claim 17 wherein each clock license  
15 includes a time value to which the clock at the destination computer is to be advanced.

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17          19. (Unamended) The method of claim 18 wherein each clock license  
18 includes an expiration time after which the clock license is invalid.

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37. (Once Amended) In a computer system, a computer-readable storage medium holding a logical structure that encapsulates components comprising:  
multiple streams of data wherein the streams of data are stored in packets;  
clock licenses that each dictate advancement of a clock that regulates rendering of the data in the packets.

38. (Unamended) The computer-readable storage medium of claim 37 wherein each clock license includes a time value to which the clock at the destination computer is to be advanced.

39. (Unamended) The computer-readable storage medium of claim 38 wherein each clock license includes an expiration time after which the clock license is invalid.

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42. (Once Amended) A data processing system comprising:  
a source computer with a storage;  
a logical structure stored in storage for encapsulating multiple data streams, data from said data streams being incorporated in packets;  
a clock license being encapsulated into at least one packet for advancing a clock at a destination when processed at the destination.

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2 Add new Claims 43-66:

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4 43. (New) The method as defined in Claim 17, wherein:  
5 the providing a logical structure for encapsulating multiple streams of data  
6 comprises:  
7 storing samples of data from multiple data streams in the packets;  
8 storing replicas of information in at least some of the packets;  
9 storing error correcting data in the at least some of the packets, wherein  
10 the error correcting data identifies an error correcting method for the at least some  
11 of the packets;  
12 setting a flag in the packets that hold the replicas to indicate that the  
13 packets hold replicas; and  
14 encapsulating the packets into the logical structure, wherein at least some  
15 of the packets hold the replicas;  
16 the method further comprises transmitting the packets of the logical structure on a  
17 packet-by-packet basis over a packet switched network from the source computer to the  
18 destination computer.  
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1           44.   (New) The method as defined in Claim 43, wherein the replicas of  
2 information hold property information regarding the samples of data.

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4           45.   (New) The method of claim 43 wherein portions of a sample are stored in  
5 selected packets and a replica of property information regarding the sample is stored in  
6 each packet in which a portion of the sample is stored.

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8           46.   (New)       The method of claim 43, further comprising the step of  
9 examining one of the replicas of information at the destination computer when one of the  
10 packets is lost during the transmitting.

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12          47.   (New) The method of claim 43, further comprising using the error  
13 correcting data in the at least some of the packets to correct an error when the transmitted  
14 logical structure is received at the destination.

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16          48.   (New) The method of claim 43, wherein:  
17 the logical structure includes a header section and a data section; and  
18 the error correcting data is stored in multiple packets in the data section.  
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1           49.   (New) The method of claim 48, wherein information in the header section  
2 of the logical structure indicates what error correcting methodology is used with the error  
3 correcting data stored in the multiple packets in the data section.

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5           50.   (New) The method of Claim 48, wherein the header section holds  
6 information regarding multiple error correcting methods.

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8           51.   (New) The method of claim 43, wherein the error correcting data identifies  
9 one of a plurality of error correcting methods.

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11          52.   (New) The method of claim 43, wherein the error correcting data holds  
12 parity bits.

13          53.   (New) The computer-readable storage medium as defined in claim 37,  
14 wherein:

15           the streams of data stored in the packets are samples of data from multiple data  
16 streams in packets for transmission on a packet-by-packet basis over a packet switched  
17 network;

18           replicas of information are stored in at least some of the packets;

19           error correcting data is stored in the at least some of the packets;  
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1 the error correcting data identifies an error correcting method for the at least some  
2 of the packets; and

3 a flag is stored in each said packet that holds the replicas.  
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5 54. (New) The computer-readable storage medium of claim 53 wherein  
6 portions of a sample are stored in selected packets and a replica of property information  
7 regarding the sample is stored in each packet in which a portion of the sample is stored.  
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9 55. (New) The computer-readable storage medium as defined in claim 53,  
10 wherein:

11 the logical structure includes a header section and a data section, and

12 the error correcting data is stored in multiple packets in the data section.  
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14 56. (New) The computer-readable storage medium as defined in claim 55,  
15 wherein the information in the header section of the logical structure indicates what error  
16 correcting methodology is used with the error correcting data stored in the multiple  
17 packets in the data section.  
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1           57.   (New) The computer-readable storage medium as defined in claim 56,  
2 wherein the header section holds information regarding multiple error correcting  
3 methods.

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5           58.   (New) The computer-readable storage medium as defined in claim 53,  
6 wherein the error correcting data identifies a plurality of error correcting methods.

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8           59.   (New) The computer-readable storage medium as defined in claim 53,  
9 wherein the error correcting data holds parity bits.

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11          60.   (New) The data processing system as defined in claim 42, wherein:  
12          the streams of data stored in the packets are samples of data from multiple data  
13 streams in the packets for transmission on a packet-by-packet basis over a packet  
14 switched network;

15          replicas of information are stored in at least some of the packets;  
16          error correcting data is stored in the at least some of the packets;  
17          the error correcting data identifies an error correcting method for the at least some  
18 of the packets; and

19          a flag is stored in each said packet that holds the replicas.  
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1           61.   (New) The data processing system of claim 42 wherein portions of a  
2 sample are stored in selected packets and a replica of property information regarding the  
3 sample is stored in each packet in which a portion of the sample is stored.  
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5           62.   (New) The data processing system as defined in claim 42, wherein:  
6           the logical structure includes a header section and a data section, and  
7           the error correcting data is stored in multiple packets in the data section.  
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9           63.   (New) The data processing system as defined in claim 62, wherein  
10 information in the header section of the logical structure indicates what error correcting  
11 methodology is used with the error correcting data stored in the multiple packets in the  
12 data section.  
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14           64.   (New) The data processing system as defined in claim 63, wherein the  
15 header section holds information regarding multiple error correcting methods.  
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17           65.   (New) The data processing system as defined in claim 42, wherein the  
18 error correcting data identifies a plurality of error correcting methods.  
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66. (New) The data processing system as defined in claim 42, wherein the error correcting data holds parity bits.

ELECTION

On Pages 2-3 of the Office Action, the claims were determined to be directed to the following plurality of patentably distinct inventions:

Group I: Claims 17-19, 38, 39, 42, drawn to a computer system having a clock \* \* \* , storing clock licenses in each packet, classified in class 370, subclass 229;

Group II: Claims 20, 21(1), 22-23, 40, drawn to a distributed system \* \* \* specify a packet size of multiple streams, classified in class 709, subclass 231.

Group III: Claims 24-26, drawn to a data processing system \* \* \* , storing an identifier of new media type, classified in class 348, subclass 385.

In response to the restriction requirement contained in the Office Action, the Applicant hereby elects without traverse for prosecution purposes the claims of Group I, namely Claims 17-19, 38-39 and 42. Additionally, Applicant elects claim 37 of Group I which, as understood, was inadvertently omitted from the restriction requirement set forth in the Office Action.

REMARKS

1. Claims 17-26, 37-40 and 42 were pending. Claims 20-26 and 40 have been cancelled. Claims 17-19, 37-39 and 42 have been elected. Claims 37 and 42 have been amended. Claims 43-66 have been added. Claims 17-19, 37-39, and 42-66 are now pending. Examination and consideration of the application, as amended, at an early date